

Graphic Era (Deemed to be University), Dehradun
Department of Electronics and Communication Engineering

TIME TABLE: 02

B.Tech. (Electronics and Communication Engineering/ESR/IoT), V Sem

Classroom: B. Tech Block LT-14

w.e.f. Aug 10, 2026

Time/Day	8:00 AM- 8:55 AM	8:55 AM- 9:50 AM	10:10 AM- 11:05 AM	11:05 AM- 12:00 PM	12:00 PM- 12:55 PM	12:55 PM- 1:55 PM	1:55 PM- 2:50 PM	3:10 PM- 4:05 PM
MON	TEC 504	TEC 501	PEC 501		XCS 501	L U N C H	TEC 551	TEC 591
TUE	XCS 501	TEC 502	TEC 504	TEC 541	TEC 503		PEC 503	
WED	TEC 502	TEC 501	TEC 503	TEC 504	TEC 541		TEC 551	TEC 591/ PEC 504
THU	TEC 501	TEC 502	PEC 502		TEC 541		TEC 503	TEC 504
FRI	XCS 501	TEC 541	TEC 502	TEC 501	TEC 503		TEC 551	TEC 591 /PEC 504

Class Coordinator: Dr. Vinay Kumar / Prof. (Dr.) Anurag Vidyarathi

SWAYAM Coordinator: Dr. Gopal Krishna Dwivedi

Code	Subject:	Concerned Faculty:	Load (L.T.P)
TEC 501	Digital Signal Processing	Prof. (Dr.) Shalini Singh	3-0-0
TEC 502	Communication Systems II	Prof. (Dr.) S. Malathi	3-0-0
TEC 503	Microcontroller and Embedded System	Prof. (Dr.) Vikas Rathi	3-0-0
TEC 541	Control Systems	Dr. Vinay Kumar	3-0-0
TEC 504	Antenna & Wave Propagation	Prof. (Dr.) Anurag Vidyarathi	3-0-0
XCS 501	Career Skills	MS. Medhavi Vishnoi (Verbal)/ Ms. Shweta (Quant)/ Dr. Gopal Krishna Dwivedi (Swayam Soft skills)	2-0-0
TEC 551	Sensor Technology	Prof. (Dr.) Varij Panwar	3-0-0
TEC 591	Transducers, Actuators and Display Devices	Dr. Rajeev Kumar	3-0-0
Laboratories			
PEC 501	Digital Signal Processing Lab	Prof. (Dr.) Shalini Singh	0-0-2
PEC 502	Communication Systems II Lab	Prof. (Dr.) S. Malathi	0-0-2
PEC 503	Microcontroller & Embedded Lab	Prof. (Dr.) Vikas Rathi	0-0-2
PEC 504	Mini Project II	Dr. Vinay Kumar	0-0-2

Dr. Kaushal Kumar
Timetable Coordinator

Dr. Abhay Sharma
Head, ECE Dept.