

GRAPHIC ERA (DEEMED TO BE UNIVERSITY), DEHRADUN
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
TIME TABLE –B.TECH (ELECTRONICS AND COMMUNICATION ENGINEERING),
VIII SEMESTER (WORKING PROFESSIONALS)

B.Tech Block (Project Lab)

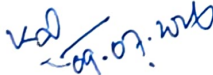
Revised Time Table: No. 00

w.e.f.11thJuly2026

Time /Day	9:00 AM-9: 55AM	10:10AM-12:00 PM	12:00 PM-1:50 PM	1:50 PM-2:30PM	2:30PM-3:25PM	3:25PM-4:20PM	5:55PM-6:50 PM	6:50PM-7:45PM	7:45PM-8:40PM	8:40PM-9:35PM
MON				L U N C H			PEC801			
TUE							PEC801			
WED							TEC853	TEC855	TOE810	
THU							PEC801			
FRI							PEC801			
SAT	TOE810	TEC855	TEC853			PEC801	PEC801			

Class Coordinator: Dr. Abhay Sharma/ Dr. Mridul Gupta

Code	Subject:	Concerned Faculty:	Load (L.T.P)
TOE810	Principles of Management (Open Elective III)	Dr. Mridul Gupta	3-0-0
TEC853	Digital System using VHDL (Program Elective V)	Dr. Abhay Sharma	3-0-0
TEC855	Telecommunication Switching (Program Elective VI)	Dr. Kaushal Kumar	3-0-0
Laboratories			
PEC801	Project Phase-II	Dr. Abhay Sharma/ Dr. Mridul Gupta	0-0-18
GP801	General Proficiency	Dr. Abhay Sharma/ Dr. Mridul Gupta	0-0-2


Dr.KaushalKumar
TimetableCoordinator


Prof.(Dr.)Md. IrfanulHasan
Head,ECE Dept.