

Graphic Era (Deemed to be University), Dehradun
Department of Electronics and Communication Engineering

TIME TABLE

B.Tech. (Electronics and Communication Engineering/ESR/IoT), VII Sem

Classroom: B. Tech Block LT-14

w.e.f. July 13, 2026

Time /Day	09:00 AM-09:55 AM	09:55 AM-10:50 AM	11:05AM-12:00 PM	12:00PM-1:00 PM	1:00 PM-1:55 PM	1:55 PM-2:50 PM	3:10 PM-4:05 PM	4:05 PM-5:00 PM	5:55 PM-6:50 PM	6:50 PM-7:45 PM	7:45 PM-8:40 PM	8:40PM-9:35 PM
MON									TEC 502		PEC 502	
TUE									TEC 501	PEC 501		
WED									TEC 503	TEC 501	TEC 504	
THU									TEC 504	TEC 541		
FRI									TEC 502	XCS 501	XCS 501	
	PEC 503		TEC 503		LUNCH	TEC 541	TEC 501	PEC 504				

Class Coordinator: Dr. Vinay Kumar / Dr. Mridul Gupta

Code	Subject:	Concerned Faculty:	Load (L.T.P)
TEC 501	Digital Signal Processing	Dr. Mridul Gupta	3-0-0
TEC 502	Communication Systems II	Ms. Neha Belwal	3-0-0
TEC 503	Microcontroller and Embedded System	Prof. (Dr.) Vikas Rathi	3-0-0
TEC 541	Control Systems	Dr. Vinay Kumar	3-0-0
TEC 504	Antenna & Wave Propagation	Dr. Anurag Vidyarthi	3-0-0
XCS 501	Career Skills	PDCC Department	2-0-0
Laboratories			
PEC 501	Digital Signal Processing Lab	Dr. Mridul Gupta	0-0-2
PEC 502	Communication Systems II Lab	Ms. Neha Belwal	0-0-2
PEC 503	Microcontroller & Embedded Lab	Prof. (Dr.) Vikas Rathi	0-0-2
PEC 504	Mini Project II	Dr. Vinay Kumar	0-0-2

Dr. Kaushal Kumar
Timetable Coordinator

Prof. (Dr.) Md. Irfanul Hasan
Head, ECE Dept.