

Graphic Era (Deemed to be University), Dehradun
Department of Electronics and Communication Engineering

TIME TABLE

B.Tech. (Electronics and Communication Engineering/ESR/IoT), VII Sem

Classroom: B. Tech Block LT-14

w.e.f. July 13, 2026

Time /Day	10:10 AM-11:05 AM	11:05 AM-12:00 PM	12:00 PM-1:00 PM		1:00 PM-1:55 PM	1:55 PM-2:50 PM	3:10 PM-4:05 PM	4:05 PM-5:00 PM	5:55 PM-6:50 PM	6:50 PM-7:45 PM	7:45 PM-8:40 PM
MON									PEC 701		
TUE									PEC 701		
WED									TEC 701	TEC 743	
THU									TEC 743	TEC 701	
FRI									PEC 701		
SAT	SEC 701		SEC 701		PEC 701						

Class Coordinator: Dr. Vinay Kumar / Dr.Abhay Sharma

Code	Subject:	Concerned Faculty:	Load (L.T.P)
TEC 701	Computer Architecture	Dr. Abhay Sharma	3-0-0
TEC 743	Design of Analog CMOS Circuit	Dr. Vinay Kumar	3-0-0
TCS 756	Human Computer Interaction	iLearn Portal	3-0-0
Laboratories			
PEC 701	Project Phase-I	Dr. Vinay Kumar	0-0-10
SEC 701	Seminar on Industrial Training	Dr. Abhay Sharma	0-0-2

Dr. Kaushal Kumar
Timetable Coordinator

Prof. (Dr.) Md. Irfanul Hasan
Head, ECE Dept.