

GRAPHIC ERA (DEEMED TO BE UNIVERSITY), DEHRADUN
DEPARTMENT OF ELECTRONICS& COMMUNICATION ENGINEERING
TIME TABLE – B. TECH (ELECTRONICS AND COMMUNICATION ENGINEERING),
VII SEMESTER (WORKING PROFESSIONALS)

B.Tech Block EC Lab 07

Time Table

w.e.f. 28 Nov, 2026

Time/Day	8:05 AM-9:00AM	9:00 AM-9: 55 AM	9:55 AM-10:50 AM	11:05 AM-12:00 PM	12:00 PM-12:55 PM	12:55 PM-1:25 PM	1:25 PM-2:20 PM	2:20 PM-3:15 PM	3:20 PM-4:15 PM	4:15 PM-5:10 PM	5:55 PM-6:50 PM	6:50 PM-7:45 PM	7:45 PM-8:40 PM
MON						L U N C H					PEC 701		
TUE											PEC 701		
WED											PEC 701		
THU											SEC 701	PEC 701	
FRI											TEC 701	TEC 751	TEC 755
SAT	SEC 701	TEC 751		TEC 755			TEC 701		MC 701	PEC 701			

Class Coordinator: Dr. Anzar Ahmad

Code	Subject	Concerned Faculty	Load (L.T.P)
TEC 701	Computer Architecture	Dr. Rajeev Kumar	3-0-0
TEC 751	Optical Fiber Communications (Elective-III)	Dr. Niraj Kumar	3-0-0
TEC 755	Wireless Sensor Network (Elective-IV)	Dr. Anzar Ahmad	3-0-0
MC 701	Disaster Management	EVS Dept.	3-0-0
Laboratories			
PEC 701	Project Phase-I	Dr. Anzar Ahmad	0-0-10
SEC 701	Seminar on Industrial Training	Dr. Rajeev Kumar	0-0-2

Dr. Kaushal Kumar
Timetable Coordinator

Prof. (Dr.) Md. Irfanul Hasan
Head, ECE Dept.