

**GRAPHIC ERA (DEEMED TO BE UNIVERSITY), DEHRADUN**  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**  
**TIME TABLE – B. TECH (ELECTRONICS AND COMMUNICATION ENGINEERING),**  
**V SEMESTER (WORKING PROFESSIONALS)**

**B.Tech Block (EC LAB 7)**

**Time Table: No. 1**

**w.e.f. 19<sup>th</sup> July 2025**

Time/ Day	8:30 AM- 10:20 AM	10:40 AM- 12:30 PM	12:30 PM- 01:25 PM	1:25 PM- 2:00 PM	2:00 PM- 2:55 PM	2:55 PM- 3:50 PM	3:50 PM- 5:40 PM	5:55 PM- 6:50 PM	6:50 PM- 7:45 PM	7:55 PM- 8:50 PM	8:50 PM- 9:45 PM
MON				L U N C H				PEC 502		TEC 501	
TUE								TEC 501	PEC 501		
WED								TEC 502	TEC 504		
THU								TEC 503	TEC 541		
FRI								XCS 501		TEC 504	
SAT	TEC 503	PEC 503	TEC 502			TEC 541	TEC 502	PEC 504			

**Class Coordinator:** Dr. Vinay Kumar/ Ms. Neha Belwal

Code	Subject:	Concerned Faculty:	Load (L.T.P)
TEC 501	Digital Signal Processing	Prof. (Dr.) Shalini Singh	3-1-0
TEC 502	Communication Systems II	Prof. (Dr.) Md. Irfanul Hasan	3-1-0
TEC 503	Microcontroller and Embedded System	Prof. (Dr.) Vikas Rathi	3-0-0
TEC 541	Control Systems	Dr. Vinay Kumar	3-1-0
TEC 504	Antenna & Wave Propagation	Prof. (Dr.) Anurag Vidyarthi	3-0-0
XCS 501	Career Skills	Quant: Mr. Abhinav Sharma and Mr. Shobhit Garg Verbal- Mr. Vibhakar Ghosh	2-0-0
<b>Laboratories</b>			
PEC 501	Digital Signal Processing Lab	Prof. (Dr.) Shalini Singh	0-0-2
PEC 502	Communication Systems II Lab	Ms. Neha Belwal	0-0-2
PEC 503	Microcontroller & Embedded Lab	Prof. (Dr.) Vikas Rathi	0-0-2
PEC 504	Mini Project	Dr. Vinay Kumar	

Dr. Kaushal Kumar  
**Timetable Coordinator**

Prof. (Dr.) Md. Irfanul Hasan  
**Head, ECE Dept.**