

**GRAPHIC ERA (DEEMED TO BE UNIVERSITY), DEHRADUN**  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**  
**TIME TABLE – B. TECH (ELECTRONICS AND COMMUNICATION ENGINEERING),**  
**V SEMESTER (WORKING PROFESSIONALS)**

**B.Tech Block (EC LAB 7)**

**Time Table: No. 2**

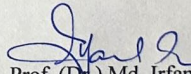
**w.e.f. 28<sup>th</sup> Dec 2024**

Time / Day	8:30 AM-10:20AM	10:35 AM-1:20 PM	1:20 PM-1:50 PM	1:50 PM-2:45 PM	2:45 PM-3:40 PM	3:40 PM-4:35 PM	4:35 PM-5:30 PM	5:30 PM-6:25 PM	6:05 PM-7:00 PM	7:00 PM-7:55 PM	7:55 PM-8:50 PM	8:50PM-9:45
MON			L U N C H						TEC 501		TEC 504	
TUE									TEC 503		PEC 503	
WED									TEC 552		TEC 504	
THU									TEC 501		PEC 501	
FRI									TEC 502		XCS 501	
SAT	TEC 503	TEC 502			XCS 501	TEC 552		PEC 502				

Class Coordinator: Dr. Vinay Kumar/ Dr.Vikas Rathi

Code	Subject:	Concerned Faculty:	Load (L.T.P)
TEC 501	Digital Signal Processing	Prof. (Dr.) Shalini Singh	3-1-0
TEC 502	Communication Systems II	Prof. (Dr.) Md. Irfanul Hasan	3-1-0
TEC 503	Microcontroller and Embedded System	Dr. Vikas Rathi	3-0-0
TEC 552	Control Systems	Dr. Vinay Kumar	3-1-0
TEC 504	Antenna & Wave Propagation	Prof. (Dr.) Rishi Prakash	3-0-0
XCS 501	Career Skills	Quant: Mr. Abhinav Sharma and Mr. Shobhit Garg Verbal- Ms. Medhavi Vishnoi	2-0-0
<b>Laboratories</b>			
PEC 501	Digital Signal Processing Lab	Prof. (Dr.) Shalini Singh	0-0-2
PEC 502	Communication Systems II Lab	Dr. Devesh Shukla	0-0-2
PEC 503	Microcontroller & Embedded Lab	Dr. Vikas Rathi	0-0-2

*VKD*  
 Dr. Kaushal Kumar  
 Timetable Coordinator

  
 Prof. (Dr.) Md. Irfanul Hasan  
 Head, ECE Dept.