

GRAPHIC ERA (DEEMED TO BE UNIVERSITY), DEHRADUN
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
TIME TABLE –B.TECH (ELECTRONICS AND COMMUNICATION ENGINEERING), III SEMESTER
(WORKING PROFESSIONALS)

LT-4 (B.Tech BLOCK)

w.e.f. 1st December 2023

Time/ Day	8:00AM- 9:50AM	10:10 AM- 12:00 PM	12:05 PM- 1:00 PM	1:00 PM- 1:55 PM	1:55 PM- 3:45 PM	4:05 PM- 5:00 PM	6:00 PM- 7:00 PM	7:00 PM- 8:00 PM	8:00 PM- 9:00 PM	
MON			L U N C H				TMA 310			
TUE							TEC 301	PEC 301		
WED							TEC 302	PEC 302		
THU							XCS 301		TMA 310	
FRI							TEC 303			
SAT	TEC 301	TEC 304			TEC 304	TEC 302	TEC 303			

Class Coordinator: Dr. Varun Mishra/ Dr. Vikas Rathi

Code	Subject:	Concerned Faculty:	Load (L.T.P)
TEC-301	Electronics Devices and Circuits	Dr. Varun Mishra	3-0-0
TEC-302	Digital Electronics	Dr. Vikas Rathi	3-0-0
TEC-303	Network Analysis and Synthesis	Dr. Devesh Shukla	3-0-0
TEC-304	Signals and Systems	Prof. (Dr.) Md. Irfanul Hasan	3-0-0
TMA-310	Advanced Engineering Mathematics	Dr. Mukesh Kumar	3-0-0
XCS-301	Career Skills	Verbal: Ms. Medhavi Vishnoi and Mr. Vibhakar Ghosh Quant: Mr. Abhinav Sharma and Mr. Shobhit Garg.	2-0-0
Laboratories			
PEC-301	Electronics Circuit Lab	Dr. Varun Mishra	0-0-2
PEC-302	Digital Electronics Lab	Dr. Vikas Rathi	0-0-2



Dr. Vinay Kumar
Timetable Coordinator



Prof. (Dr.) Md. Irfanul Hasan
Head, ECE Dept.