

**GRAPHIC ERA (DEEMED TO BE UNIVERSITY), DEHRADUN**  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**  
**TIME TABLE (Offline), M.TECH (VLSI Design and Systems), Sem-III**

Classroom: Project Lab

w.e.f. July 27, 2023

Time/ Day	10:10AM- 11:05M	11:05 AM- 12:00 PM	12:00 PM- 1:00PM	1:00 PM- 1:55 PM	1:55PM- 2:50PM	3:10 PM- 4:05 PM	4:05 PM-5:05 PM
MON	VDM 301		VDM 391	<b>L U N C H</b>	VDM 301		
TUE	VDM 391	VOM 302	VDM 301		VDM 301		
WED	VOM 302	VDM 351			VOM 302	VDM 301	
THU	VDM 351		VDM 391		VDM 301		
FRI	VOM 302	VDM 391			VDM 301		

Class Coordinator: Mr. Kamlesh Kukreti

Code	Subject:	Concerned Faculty:	Load (L.T.P)
VDM 391	Organic Electronics Devices and Circuits	Prof. (Dr) Varij Panwar	3-1-0
VOM 302	Internet of Things	Dr. Gaurav Verma	3-1-0
<b>Laboratories</b>			
VDM 351	Modeling and Simulation Lab	Mr. Kamlesh Kukreti	0-0-2
VDM 301	Dissertation Phase- I	Mr. Kamlesh Kukreti	0-0-2

Dr. Vinay Kumar  
**Timetable Coordinator**

Prof. (Dr.) Md. Irfanul Hasan  
**HOD (ECE)**