



Department of Electronics and Communication Engineering

Kind attention to M. Tech (VLSI Design and Systems)

End Semester Practical Exam Schedule (Ist Semester)

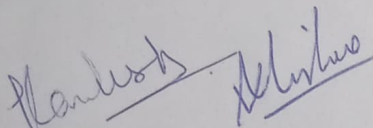
December 29, 2022

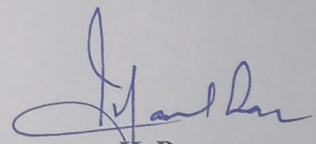
Notice

End semester Practical examination has been scheduled from 7th January 2023 Onwards.
Students must maintain the following schedule:

Date	Time of Examination	Lab Name
10-01-2023	10:00 am Onwards	CMOS Circuit Design Lab (VDM 151)
11-01-2023	10:00 am Onwards	Digital VLSI Circuit Design Lab (VDM 152)

Note: Students are required to bring their record file to the lab.


Faculty In charge


HoD
Dept of ECE