

GRAPHIC ERA (DEEMED TO BE UNIVERSITY), DEHRADUN
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
TIME TABLE – 4, B.TECH (ELECTRONICS AND COMMUNICATION ENGINEERING, ESR*/IoT*), III Sem

Room: Project Lab

From 21st Nov to 26th Nov 2022

Time/ Day	8:00AM- 8:55AM	11:10AM- 12:05 PM	12:05 PM- 1:00 PM	1:00 PM- 2:00 PM
MON	TEC 303	TMA 310	TEC 304	L
TUE	TEC 302	TMA 310	TEC 301	U
WED	TEC 303	TEC 304	TEC 301	N
THU	TEC 301	TEC 304	TMA 310	C
FRI	XCS 301	TEC 303	TEC 302	
SAT	TEC 302	TEC 359	TEC 359	H

Class Coordinator: Dr. Vikas Rathi/ Dr. Varun Mishra

MOOCs Coordinator: Dr. Vikas Rathi / Dr. Varun Mishra

Code	Subject Name	Concerned Faculty	Load (L.T.P)
TEC 301	Electronic Devices and Circuits	Dr. Varun Mishra	3-1-0
TEC 302	Digital Electronics	Dr. Vikas Rathi	3-1-0
TEC 303	Networks Analysis and Synthesis	Dr. U. M. Bhatt	3-1-0
TEC 304	Signals and Systems	Prof. (Dr.) Md. Irfanul Hasan	3-1-0
TMA 310	Advanced Engg. Mathematics	Dr. Mukesh Kumar	3-1-0
TEC 359*	Fundamentals of Computer Organization (IoT and ESR)	Ms. Sribidhya Mohanty	3-1-0
XCS 301	Career Skills	Ms. Jyoti Joshi and Mr. Deepak Chauhan	2-0-0
Laboratories			
PEC 301	Electronics Circuit Lab	Dr. Varun Mishra	0-0-2
PEC 302	Digital Electronics Lab	Dr. Vikas Rathi	0-0-2
PEC 303	Networks Lab	Dr. U. M. Bhatt	0-0-2

Dr. Mridul Gupta
Timetable Coordinator

Prof. (Dr.) Md. Irfanul Hasan
HOD (ECE)