

Graphic Era (Deemed to be University), Dehradun
Department of Electronics and Communication Engineering

TIME TABLE – 1, M.Tech (VLSI Design and Systems), 3rd Sem

Room: EC Lab-07

w.e.f. 12th September 2022

Time/ Day	10:00 AM- 10:55 AM	10:55 AM- 11:50 PM	12:05 PM- 1:00 PM	1:00 PM- 2:00 PM	2:00 PM- 2:55 PM	2:55 PM- 3:50 PM	4:05 PM to 5:00 PM
MON	VDM 301			L U N C H	VDM 301		
TUE	VDM 391	VOM 302	VDM 301		VDM 301		
WED	VOM 302	VDM 351			VDM 301		
THU	VDM 351		VDM 391		VDM 301		
FRI	VOM 302 (EC Lab-10)	VDM 391 (EC Lab-10)	VDM 301		VDM 301		

Class Coordinator: Prof. (Dr.) Varij Panwar

Code	Subject Name	Concerned Faculty	Load (L.T.P)
VDM 391	Organic Electronics Devices and Circuits	Prof. (Dr.) Varij Panwar	3-0-0
VOM 302	Internet of Things	Dr. Gourav Verma	3-0-0
Laboratories			
VDM 351	Modelling and Simulation Lab	Mr. Kamlesh Kukreti	0-0-4
VDM 301	Dissertation Phase- I	Prof. (Dr.) Varij Panwar	0-0-20

Dr. Mridul Gupta
Timetable Coordinator

Prof. (Dr.) Md. Irfanul Hasan
HOD (ECE)