

**Graphic Era (Deemed to be University), Dehradun**  
**Department of Electronics and Communication Engineering**

**TIME TABLE – 1, M.Tech (VLSI Design and Systems), 1<sup>st</sup> Sem**

Room: Project Lab

W.e.f. 12<sup>th</sup> September 2022

Time/ Day	10:00 AM- 10:55 AM	10:55 AM- 11:50 PM	12:05 PM- 1:00 PM	1:00 PM- 2:00 PM	2:00 PM- 2:55 PM	2:55 PM- 3:50 PM
<b>MON</b>	VDM 191	VDM 151		L	Research Activities	
<b>TUE</b>	VDM 103	VDM 104	VDM 102	U	VDM 152	
<b>WED</b>	VDM 103	VDM 101	VDM 191	N	VDM 102	VDM 104
<b>THU</b>	VDM 104	VDM 101	VDM 102	C	VDM 152	
<b>FRI</b>	VDM 101	VDM 191	VDM 103	H	VDM 151	

**Class Coordinator:** Mr. Kamlesh Kukreti

Code	Subject:	Concerned Faculty:	Load (L.T.P)
VDM 101	Semiconductor Materials and Devices	Dr. Peyush Pandey	3-0-0
VDM 102	CMOS Analog Circuit Design	Dr. Chandni Tiwari	3-0-0
VDM 103	Advanced Digital Integrated Circuit	Mr. Kamlesh Kukreti	3-0-0
VDM 104	VLSI Technology	Prof. (Dr.) Santosh Saraf	3-0-0
VDM 191	Fundamentals of Nanotechnology	Prof. (Dr.) Varij Panwar	3-0-0
<b>Laboratories</b>			
VDM 151	CMOS Circuit Design Lab	Dr. Varun Mishra	0-0-4
VDM 152	Digital VLSI Circuit Design Lab	Mr. Kamlesh Kukreti	0-0-4

Dr. Mridul Gupta  
**Timetable Coordinator**

Prof. (Dr.) Md. Irfanul Hasan  
**HOD (ECE)**